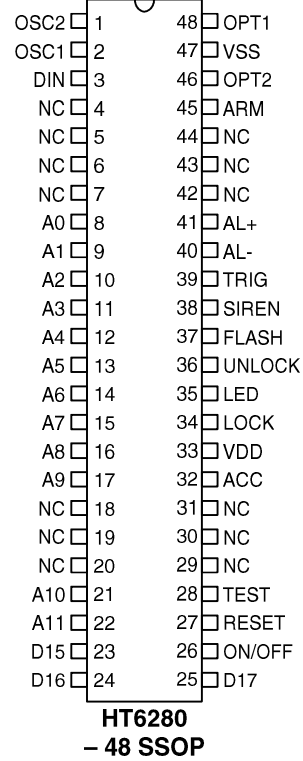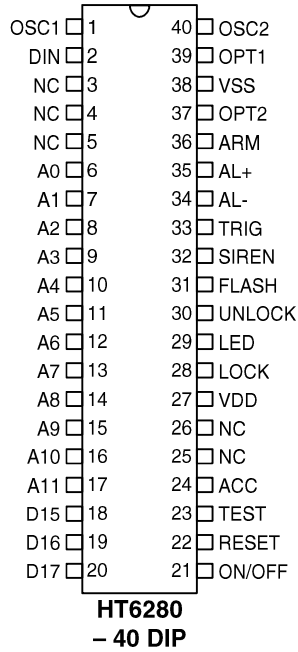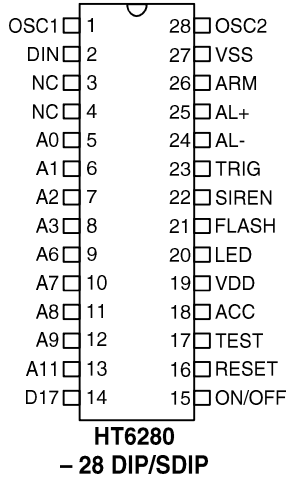## Features

- Operating voltage: 3V~5V
- Low power dissipation and high noise immunity CMOS technology
- A built-in oscillator with only a 5% resistor
- 3^{12} trinary address codes
- 6 decoder control data outputs

- 3 local alarm trigger inputs:
  - TRIG, AL+, AL-
- A valid transmission indicator (LED)
- ACC disarms the whole car alarm system
- Easily interface with an RF or an IR medium
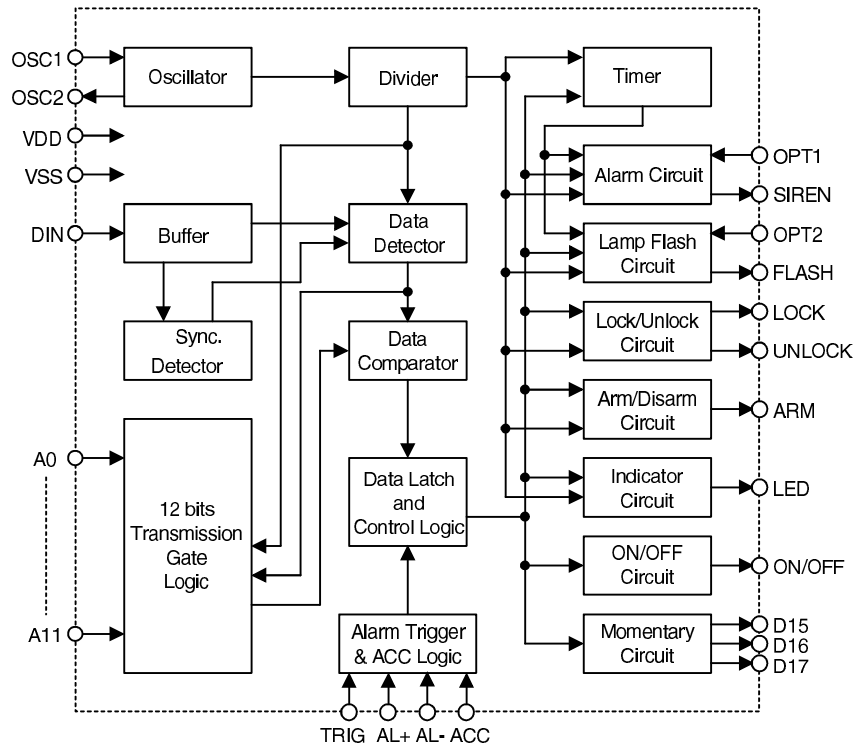
General Description

The HT6280 is an 18-bit decoder CMOS LSI designed for use in remote control car alarm system. When complemented by one of the TE triggered HT600/HT640/HT680 or DATA triggered HT6187/HT6207/HT6247 encoder ICs from a range of 3^{18} series of encoders, a complete remote car alarm system can be formed. The details on the 3^{18} encoders can be found in the relevant databook encoder section.

After receiving the 18 bits of information from the encoder the HT6280 interprets the first 12 bits as address and the last 6 bits as control signal data. If these received 12 bits of address matches that of the locally set address then the 6 data bits are latched and the control function executed. Three types of control function are incorporated in the device, these are arm/disarm, panic and power on/off. The HT6280 also provides 3 momentary data outputs for other user defined special purposes.

Pin Assignment

HT6280 – 28 DIP/SDIP

Left	Pin		Pin	Right
OSC1	1		28	OSC2
DIN	2		27	VSS
NC	3		26	ARM
NC	4		25	AL+
A0	5		24	AL-
A1	6		23	TRIG
A2	7		22	SIREN
A3	8		21	FLASH
A6	9		20	LED
A7	10		19	VDD
A8	11		18	ACC
A9	12		17	TEST
A11	13		16	RESET
D17	14		15	ON/OFF

HT6280 – 40 DIP

Left	Pin		Pin	Right
OSC1	1		40	OSC2
DIN	2		39	OPT1
NC	3		38	VSS
NC	4		37	OPT2
NC	5		36	ARM
A0	6		35	AL+
A1	7		34	AL-
A2	8		33	TRIG
A3	9		32	SIREN
A4	10		31	FLASH
A5	11		30	UNLOCK
A6	12		29	LED
A7	13		28	LOCK
A8	14		27	VDD
A9	15		26	NC
A10	16		25	NC
A11	17		24	ACC
D15	18		23	TEST
D16	19		22	RESET
D17	20		21	ON/OFF

HT6280 – 48 SSOP

| Left	Pin		Pin	Right
OSC2	1		48	OPT1
OSC1	2		47	VSS
DIN	3		46	OPT2
NC	4		45	ARM
NC	5		44	NC
NC	6		43	NC
NC	7		42	NC
A0	8		41	AL+
A1	9		40	AL-
A2	10		39	TRIG
A3	11		38	SIREN
A4	12		37	FLASH
A5	13		36	UNLOCK
A6	14		35	LED
A7	15		34	LOCK
A8	16		33	VDD
A9	17		32	ACC
NC	18		31	NC
NC	19		30	NC
NC	20		29	NC
A10	21		28	TEST
A11	22		27	RESET
D15	23		26	ON/OFF
D16	24		25	D17

Block Diagram

Pin Description

HT6280 — 40 Pins

Pin No.	Pin Name	I/O	Internal Connection	Description
1	OSC2	I	OSCILLATOR	Oscillator output pin
2	DIN	I	CMOS IN	Serial data input pin
3~5	NC	—	—	No connection
6~17	A0~A11	I	TRANSMISSION GATE	A0~A11 address inputs They can be set to VDD, VSS, or floating.
18~20	D15~D17	O	CMOS OUT	D15~D17 momentary data outputs from D15~D17 of an encoder, for external control use
21	ON/OFF	O	CMOS OUT	ON/OFF output pin for external control use Toggle outputs of the ON/OFF pin are triggered by D14 of an encoder.

Pin No.	Pin Name	I/O	Internal Connection	Description
22	RESET	I	CMOS IN	System reset pin
23	TEST	O	CMOS OUT	For IC testing only
24	ACC	I	CMOS IN Pull-Low	An active high signal to the ACC pin disarms the alarm system.
25,26	NC	—	—	No connection
27	VDD	I	—	Positive power supply
28	LOCK	O	CMOS OUT	Lock signal output to control external lock/unlock, active high
29	LED	O	NMOS OUT	LED indictor for arm/disarm, alarm, and panic
30	UNLOCK	O	CMOS OUT	Unlock signal output to control external lock/unlock, active high
31	FLASH	O	CMOS OUT	Flash output pin to control external car lamp circuit, active high
32	SIREN	O	CMOS OUT	Alarm output pin to control external siren circuit, active high
33	TRIG	I	CMOS IN Pull-High	Alarm trigger input in the arm state, active high
34	AL-	I	CMOS IN Pull-High	Alarm trigger input in the arm state, active low
35	AL+	I	CMOS IN Pull-Low	Alarm trigger input in the arm state, active high
36	ARM	O	CMOS OUT	Arm state output, active high This pin is toggled by D12 of an encoder.
37	OPT2	I	CMOS IN Pull-High	Car lamp flash selection during alarm: VSS: Lamp flash VDD or Open: Lamp flash disabled
38	VSS	I	—	Negative power supply (GND)
39	OPT1	I	CMOS IN Pull-High	Chirp sound selection during change of arm/disarm states: VSS: Chirp VDD or Open: Chirp disabled
40	OSC1	I	OSCILLATOR	Oscillator input pin

12th Apr '96

Approximate internal connection circuits
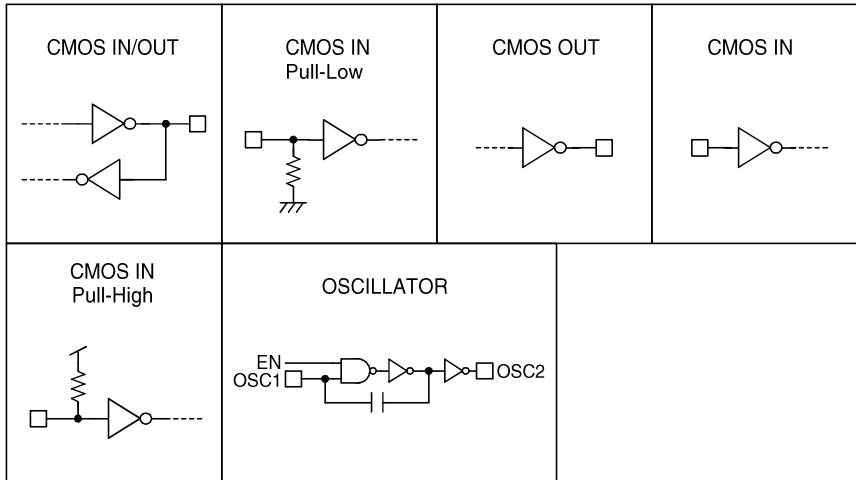
Absolute Maximum Ratings

Supply Voltage ....−0.3V to 5.5V

Input Voltage....................V_{SS}−0.3 to V_{DD}+0.3V

Storage Temperature.................−50°C to 125°C

Operating Temperature...............−20°C to 75°C

Electrical Characteristics

(Ta=25°C)

Symbol	Parameter	Test Condition		Min.	Typ.	Max.	Unit
		V_{DD}	Condition				
V_{DD}	Supply Voltage	—	—	3	—	5	V
I_{STB}	Stand-by Current	5V	Oscillator stops	—	2	4	µA
I_{DD}	Operating Current	5V	No load F_{OSC}=100KHz	—	200	400	µA
R_{PH}	Pull-High Resistance	5V	V_{IN}=0V	—	150	—	KΩ
R_{PL}	Pull-Low Resistance	5V	V_{IN}=5V	—	150	—	KΩ
I_{OUT1}	Output Current (D15~D17)	Source 5V	V_{OUT}=0.9V_{DD}	−0.3	−0.6	—	mA
		Sink 5V	V_{OUT}=0.1V_{DD}	0.5	1	—	mA
I_{OUT2}	Output Current (ON/OFF)	Source 5V	V_{OUT}=0.9V_{DD}	−0.7	−1.2	—	mA
		Sink 5V	V_{OUT}=0.1V_{DD}	0.5	1	—	mA
I_{OUT3}	Output Current (LOCK, UNLOCK)	Source 5V	V_{OUT}=0.9V_{DD}	−2	−4	—	mA
		Sink 5V	V_{OUT}=0.1V_{DD}	0.5	1	—	mA

Symbol	Parameter		Test Condition		Min.	Typ.	Max.	Unit
			V_{DD}	Condition				
I_{OUT4}	Output Current (FLASH, SIREN)	Source	5V	V_{OUT}=0.9V_{DD}	−1.5	−3	—	mA
		Sink	5V	V_{OUT}=0.1V_{DD}	0.5	1	—	mA
I_{OUT5}	Output Current (ARM)	Source	5V	V_{OUT}=0.9V_{DD}	−1	−2	—	mA
		Sink	5V	V_{OUT}=0.1V_{DD}	0.5	1	—	mA
I_{LED}	Output Current	Sink	5V	V_{OUT}=0.1V_{DD}	1	2	—	mA
V_{IH}	"H" Input Voltage		—	—	0.8V_{DD}	—	V_{DD}	V
V_{IL}	"L" Input Voltage		—	—	0	—	0.2V_{DD}	V
F_{OSC}	System Frequency		5V	R_{OSC}=200KΩ	—	100	—	KHz

Functional Description

Operation

After power on, the HT6280 receives a data word from one of the 3^{18} series of encoders. It interprets the first 12 bits of code as address and the last 6 bits as data. The decoder then compares the received address with its local address code twice successively. If the received address code match the local address code, the 6 data bits are decoded to the output pins.

Power on initial

The HT6280 is in the arm state at the inital power on.

Arm/disarm control

The ARM signal toggles the arm/disarm condition of the HT6280. If the decoder is in the arm state and a signal has been received on the ARM pin (the D12 signal of an encoder) the car lights flash twice continuously, the chirp sounds twice successively and the alarm system enters the disarm state. Similarly, if the arm/disarm signal is received again the lights also flash once, the chirp sounds once, and the alarm system enters the arm state. The timing of arm/disarm is shown in Fig.1.
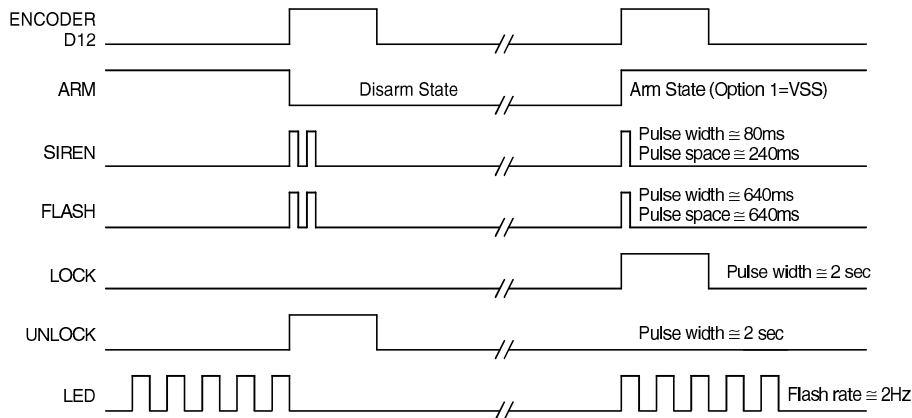

Fig.1 No panic/alarm trigger for the arm state

Alarm triggering (AL+, AL-, TRIG)

With the alarm in the arm state, any one of the three signals AL+, AL-, and TRIG can trigger the alarm. Triggering the alarm causes the lights to flash at a 2Hz rate (OPT2=VSS) and the siren to sound. If the trigger is removed within 4 cycles, the flashing lights and the siren cease at the end of that cycle; otherwise they go on to complete new 4 cycles. A cycle lasts for 30 seconds on and 10 seconds off. During an alarm trigger the LED flashes at an 8Hz rate and then continues even if the trigger is removed. Activating the ACC (the alarm reset line) will turn the LED off in addition to the lights and the siren. If the AL+, AL- and TRIG lines are disabled in the disarm state, the panic function is still enabled. The timing of the alarm trigger is shown in Fig.2.
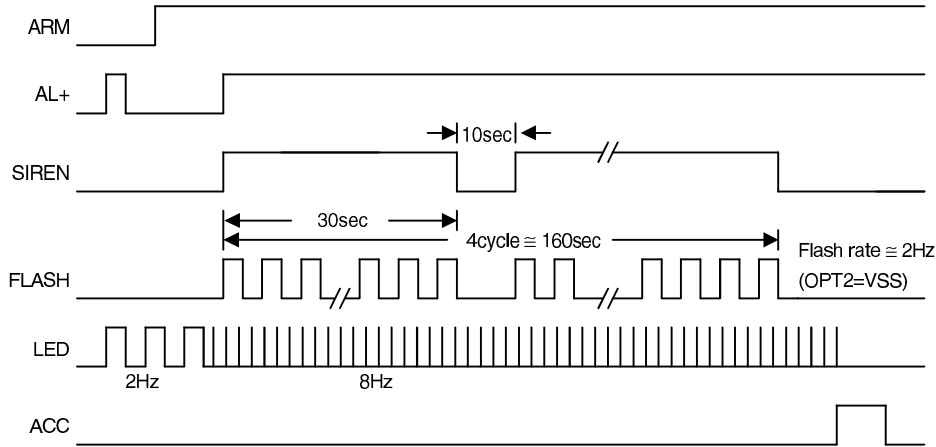
Fig.2 Alarm trigger (AL+, AL-, TRIG) for the arm state

Panic control

If a panic signal (D13) has been received whether in the arm or disarm state, the siren sounds and the flash of car lights lasts for one cycle until the ACC pin is activated. The LED will flash quickly and then turn off if ACC is activated whether the siren is in the working or stop state. The timing is shown in Fig.3.
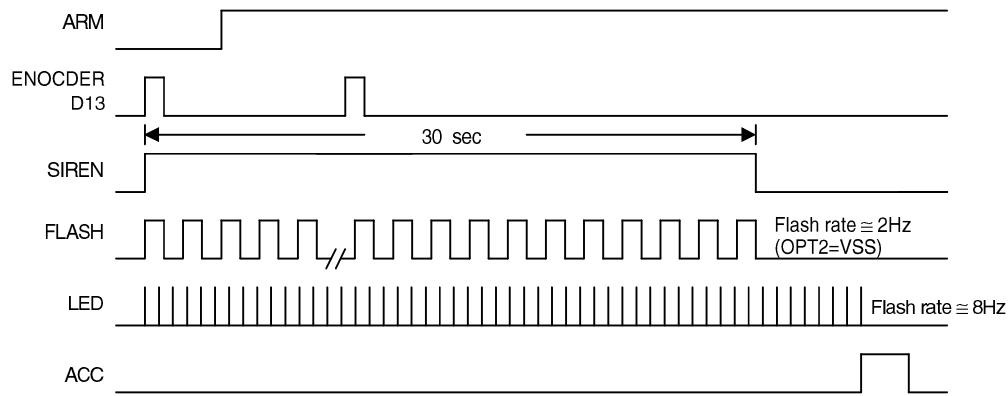
12th Apr '96

Fig.3 Panic trigger for the arm state

Alarm output (SIREN)

If a trigger (AL+, AL-, or TRIG) has been removed within a period 4 cycles, the SIREN will stop at that cycle. Otherwise it will continue to complete another period of 4 cycles. The timing of a cycle is about 30 seconds on and 10 seconds off. The alarm state is quitted by the arm/disarm (D12) trigger.

If the panic turns on (active high) whether in the arm or disarm state, the SIREN will output one cycle (30 seconds on and 10 seconds off). The alarm state is quitted by an active high ACC signal.

The SIREN outputs an 80ms pulse when the HT6280 enters the arm state. It then outputs two pulses (Pulse width=80ms Pulse space=240ms) when the decoder enters the disarm state by the arm/disarm (D12) trigger. This function can be inhibited by floating OPT1.

LOCK/UNLOCK output

The LOCK and UNLOCK pins are not activated at the initial power on. The LOCK pin outputs a 2-second pulse when the HT6280 enters the arm state. The UNLOCK pin, on the other hand, outputs a 2-second pulse when the decoder enters the disarm state. The LOCK and UNLOCK pins cannot operate simultaneously but LOCK takes priority over UNLOCK. The timing is shown in Fig.1.

FLASH output

The FLASH pin outputs a pulse when the HT6280 enters the arm state or two pulses when the decoder enters the disarm state. After the siren operates, the light will flash at a 2Hz rate until the siren ceases. This function can be inhibited by floating OPT2.

LED output

The LED pin is of high impedance at the initial power on. In the arm state the LED flashes at a 2Hz rate. But if the alarm or panic is triggered the LED flashes at an 8Hz rate until the ACC pin is activated. The timing is shown in Fig.1.

Encoder/Decoder reference table

Encoder Data	Decoder Function
D12	Arm/Disarm toggle
D13	Panic control
D14	ON/OFF toggle
D15	Momentary data output D15
D16	Momentary data output D16
D17	Momentary data output D17

ACC input

After the active high ACC input is activated, the car alarm system enters the disarm state, the arm/disarm (D12), panic, or other control signals fail to be received, and the LED stops flashing.

ARM output

The ARM outputs a "Hi" signal when the HT6280 is operating in the arm state. Otherwise it outputs a "Lo" signal.

ON/OFF output

The ON/OFF pin provides an external toggle on/off control when the HT6280 responds to the D14 signal from the encoder. The ON/OFF timing is shown in Fig.4.
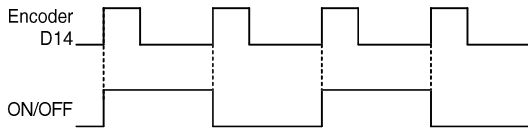
Fig.4 ON/OFF timing

D15~D17 data output

D15~D17 of the HT6280 are momentary type data outputs which follow D15~D17 of an encoder. These lines are for user-defined control use. The timing is shown in Fig.5.
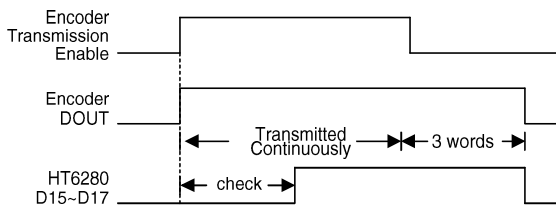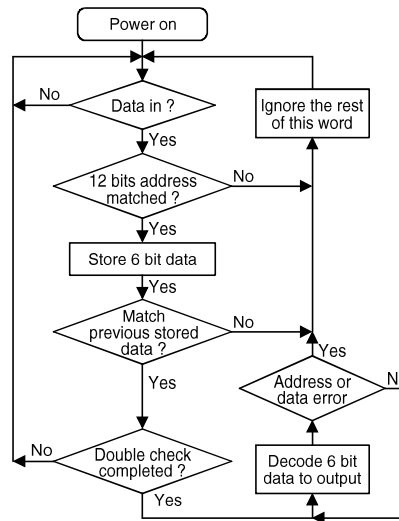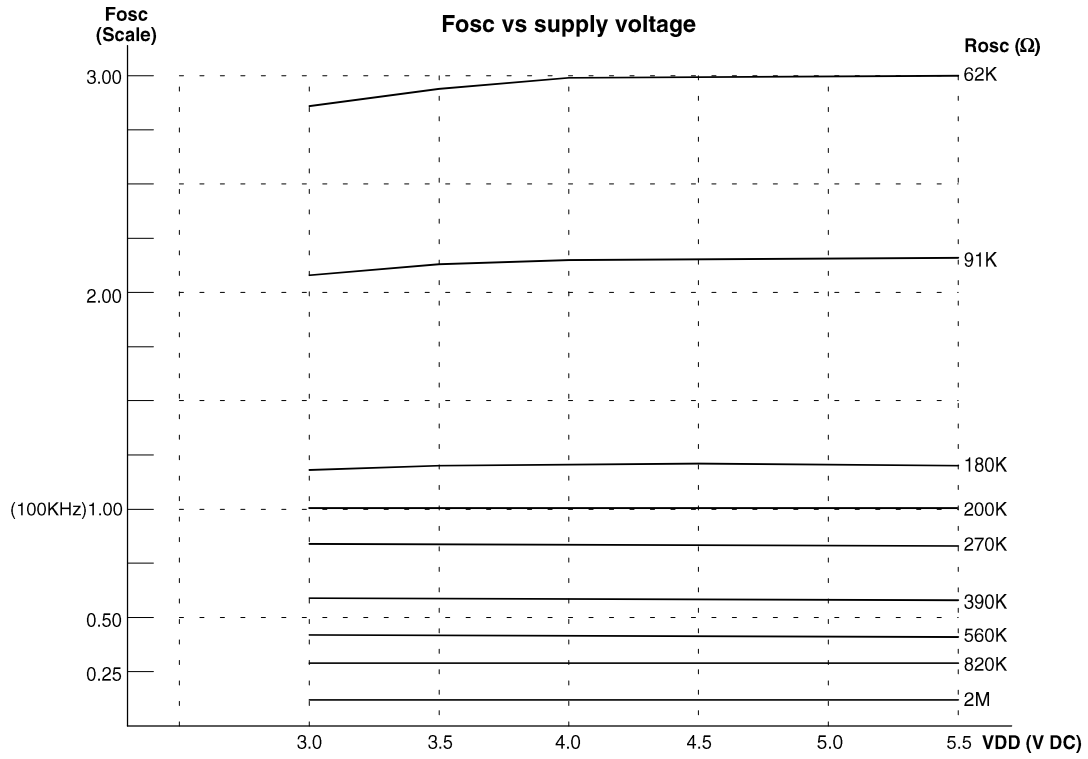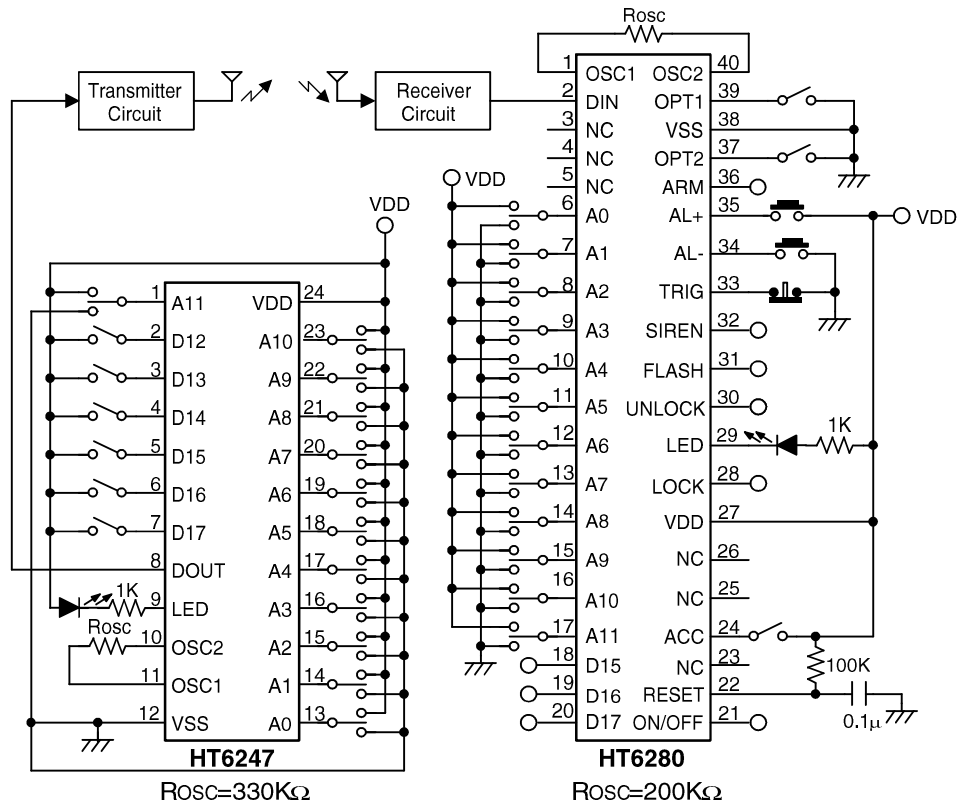

Fig.5 D15~D17 timing

Flowchart

Oscillator frequency vs. supply voltage

The recommended oscillator frequency is F_{OSCD} (decoder) $\cong F_{OSCE}$ (encoder).

Application Circuits

40 pin DIP package

HT6247
ROSC=330KΩ

HT6280
ROSC=200KΩ

Note: Typical infrared diode: EL-1L2 (KODENSHI CORP.)
Typical RF transmitter: JR-220 (JUWA CORP.)
Typical infrared receiver: PIC-12043T/PIC-12043S (KODESHI CORP.)
or LTM9052 (LITEON CORP.)
Typical RF receiver: JR-200 (JUWA CORP.)